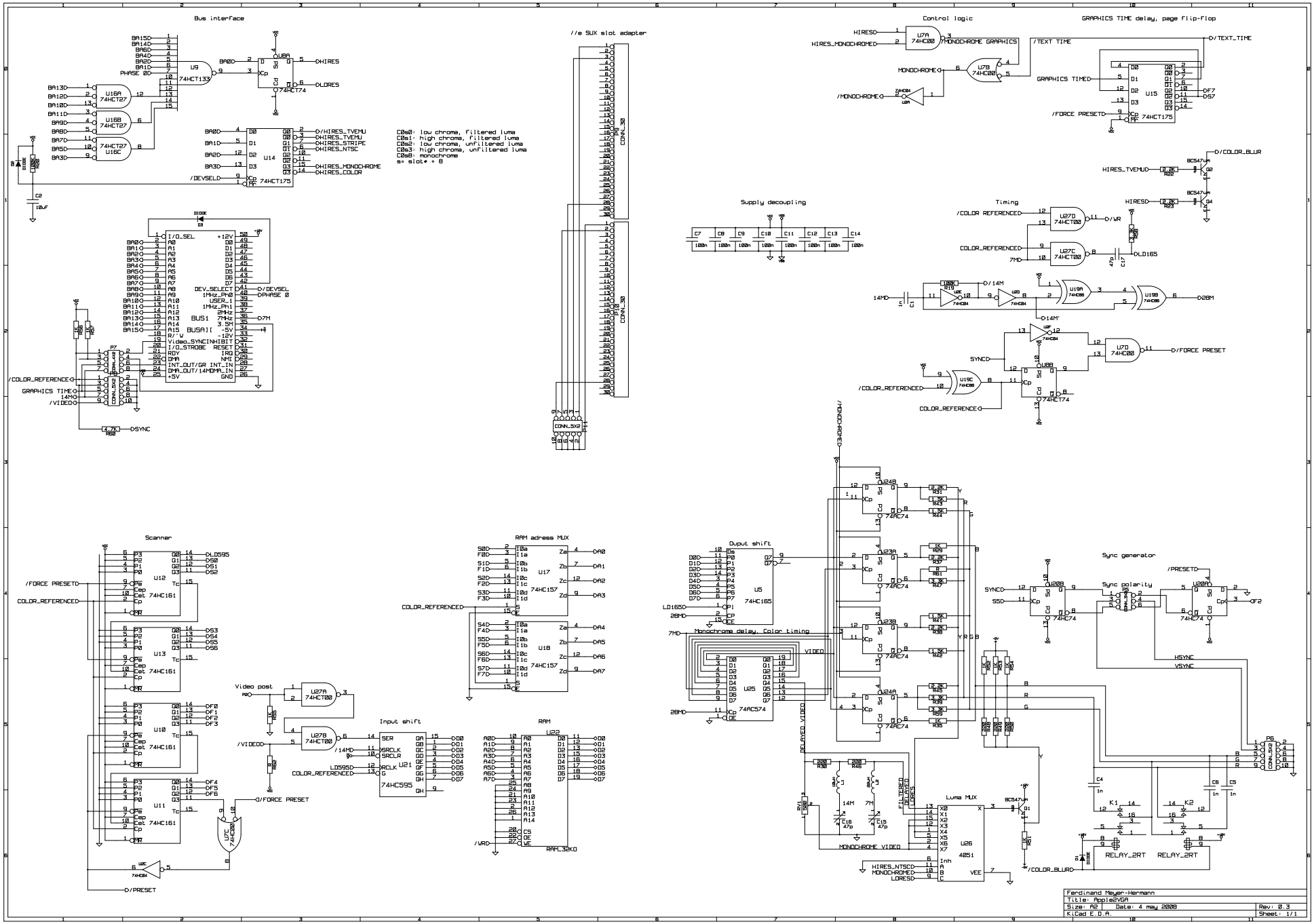
Bus interface
//e SUX slot adapter
Control logic
GRAPHICS TIME delay, page flip-flop
Supply decoupling
Timing
Scanner
RAM adress MUX
Dupt shift
Monochrome delay, Color timing
Sync generator
Sync polarity
Video post
Input shift
RAM
Luma MUX
C0a0: low chroma, filtered luma
C0a1: high chroma, filtered luma
C0a2: low chroma, unfiltered luma
C0a3: high chroma, unfiltered luma
C0a8: monochrome
s= slot* + 8
Ferdinand Meyer-Hermann
Title: Apple2VGA
Size: A2
Date: 4 may 2008
Rev: 0.3
KiCad E.D.A.
Sheet: 1/1